長庚大學 電子工程學系碩士班 必選修科目表 (113學年度入學學生適用)

Curriculum of the master's program of Dept. Electronic Engineering, Chang Gung University (applicable to students admitted in 2024)

技術	Cradit #		學分 Credi		Semeste	
Common Compulsory	0		0		0	0
Common Compulsory Presspiration Compulsory Presspiration Presspir	6		6			
College constructed courses Elective						
乗門 熟修 企業質質(I)/2)(Industry 12 = 6 6 元前の 三世では 三世では 下面が良い 三世では 下面が良い 三世では 下面が良い 三世では 三世では 下面が良い 三世では	2 -		2	_	2	2
Group A Elective Elective	3 -		3	-		3
中報	3 -		3		3	
甲組 選修 金子力學(Quantum Mechanics) 3 二 3 乙組 選修 混合原式公参數網路分析 (Crucit Analysis) 3 甲組 選修 中組 選修 十等證實驗(Semiconductor Lective Experiments) 1 — 3 —	3 -		3	-	3	
Group A Elective Experiments 1	3 -		3	_	3	
中組 選修 積機電路専論(Special Topic on VLSI Engineering)	3 -		3	-	3	
甲組 選修 子導體製程及元件模擬 (Semiconductor Device Design & Simulation) 3 二组 選修 (Semiconductor Device Design & Simulation) 3 二组 選修 (Group B Elective (Mixed-Signal IC Design)) 3 甲組 選修 (Group A Elective P44) 遊修 (機能電質験(MEMS micro fabrication Lab.) 1 - - 24	3 -		3	-	3	
甲組 選修 基礎光學(Basic Optics) 3 — 3 Z組 選修 生醫電子學(Biomedical Electronics) 3 甲組 選修 微機電實驗(MEMS micro fabrication Lab.) 1 — 1 石組 選修 上動微波電路設計(Active Microwave Circuit Design) 3 甲組 選修 后elective Elective Microwave Circuit Design) 3 — 2 五	3 -		3	_		3
甲組 選修 微機電質験(MEMS micro fabrication Lab.) 1 — 1 乙組 選修 Elective fabrication Lab.) 3 甲組 選修 Group A 生聲電子微流體系統 (Biomedical Electronic Microfluidic System) 3 — 3 乙組 選修 高等超大型積體電路設計 (Advanced VLSI Design) 3 甲組 選修 河中 A 連修 村科研究方法(Methods for Group A Elective Material Research) 3 — 3 乙組 選修 電磁車論(Electromagnetic Group B Elective Technology) 3 — 3 乙組 選修 電磁車論(Electromagnetic Group B Elective Technology) 3 — 3 </td <td>3 -</td> <td></td> <td>3</td> <td>_</td> <td></td> <td>3</td>	3 -		3	_		3
甲組 選修 告醫電子微流體系統 (Biomedical Electronic Microfluidic System) 3 3 乙組 選修 高等超大型積體電路設計 (Advanced VLSI Design) 3 甲組 選修 薄膜工程(Thin Film 	3 -		3	_		3
甲組 選修 薄膜工程(Thin Film Group A Elective Technology) 3 3 3 2組 選修 Group B Elective PCB Design) 3 3 3 3 3 3 3 2組 選修 Group B Elective PCB Design) 3 3 3 3 3 3 3 3 2組 選修 Group B Elective PCB Design) 3	3 -		3	-		3
甲組 選修 材料研究方法(Methods for Group A Elective Material Research) 3 — 3	3 -	T	3	_		3
甲組 選修 物理光學(Physical Optics) 3 — 3 乙組 選修 高等類比積體電路設計 (Advanced Analog IC Design) 3 甲組 選修 先進記憶體元件(Advanced Analog IC Design) 3 — 3 乙組 選修 超大型積體電路設計 (Advanced Analog IC Design) 3 田組 選修 Group A Elective 半導體量測(Semiconductor Measurement) 3 — 3 <	3 -	T	3	-		3
中組 選修 先進記憶體元件(Advanced Group A Elective Menory Devices) 3 — 3 乙組 医修 Group B Elective Elective Menory Devices) 超大型積體電路測試設計 (VLSI Testing and Testable Figure 1) 3 甲組 選修 Group A Elective Measurement) 半導體量測(Semiconductor Measurement) 3 — 3 乙組 運修 Group B Elective Digital Signal Processing) 3 甲組 選修 Group A Elective Filter Design A Elective Filter Design A Elective Filter Design A Elective Filter Design) 3 — 3 乙組 運修 Group B Elective Filter Design) 数位通信積體電路設計 (Digital Communication Integrated Circuit Design) 3 甲組 選修 Group A Elective Filter Design A Elective Filter Design Beleative Filter Design Beleative Filter Design) 3 — 3 乙組 運修 数位通信積體電路設計 (Digital Communication Integrated Circuit Design) 3 甲組 選修 Group A Elective Filter Design Beleative Filter Design Bele	3 -	T	3	_		3
甲組 選修 半導體量測(Semiconductor Group A Elective Measurement) 3 — 3 乙組 医修 Group B Elective Digital Signal Processing) 3 甲組 選修 Group A Elective Group A Elective Group A Elective Digital Signal Processing) 3 — 3 乙組 選修 Group B Elective Digital Signal Processing) 3 甲組 選修 Group B Elective Digital Signal Processing) 3 — 3 乙組 選修 Group B Elective Digital Communication Integrated Circuit Design) 3 甲組 選修 Group A Elective Digital Di	3 -		3	1-		3
甲組 選修 奈米材料與元件 3 — 3 基修 Group B Elective Filter Design) 3 甲組 選修 Elective 固態物理(Solid State Physics) 3 — 3 基修 Elective Filter Design) 數位通信積體電路設計 (Digital Communication Integrated Circuit Design) 甲組 選修 元件量測與可靠性(Devices Group A Elective Filter Design) 3 — 3 A 基修 Group B Elective Filter Design) 3 田台 選修 元件量測與可靠性(Devices Measurement and Reliability) 3 — 3 A A 基修 数位積體電路設計(Digital Group B Elective Integrated Circuit Design) 3 田台 選修 光電元件與系統之電性可靠 乙組 環修 超大型積體電路I信號處理設	3 -		3	1-		3
中組 Group A 選修 Elective 固態物理(Solid State Physics) 3 — 3 乙組 Group B 選修 Elective 數位通信積體電路設計 (Digital Communication Integrated Circuit Design) 甲組 Group A 選修 Elective 元件量測與可靠性(Devices Measurement and Reliability) 3 — 3 乙組 Group B 選修 Elective 數位積體電路設計(Digital B Elective 3 田組 W修 光電元件與系統之電性可靠 光電元件與系統之電性可靠 乙組 環修 選修 超大型積體電路I信號處理設	3 -		3	-		3
Group A Elective 固態物理(Solid State Physics) 3 — 3 Group B Elective Elective (Digital Communication Integrated Circuit Design) 甲組 選修 Group A 元件量測與可靠性(Devices Group B Elective Measurement and Reliability) 3 — 3 乙組 選修 Group B Elective Elective Integrated Circuit Design) 動位機體電路設計(Digital Integrated Circuit Design) 田台 光電元件與系統之電性可靠 乙組 環條 超大型積體電路I信號處理設		1				2
Group A Elective Measurement and Reliability) 3 3 Group B Elective Integrated Circuit Design) 出租 業務 光電元件與系統之電性可靠 乙組 業務 超大型積體電路I信號處理設	3 -		3			3
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中語 医能 医(Electrical reliability of opto- electronic components and systems) 3 - 3 日本社 Group B Elective 計(VLSI Digital Signal 3 Processing Design) 3	3 =		3	=	3	
甲組 選修 高速半導體元件(Hign Speed 3 - 3 Z組 選修 類比積體電路設計(Analog Group A Elective Semiconductor Devices) 3 - 3 Group B Elective Integrated Circuit Design) 3	3 =		3	=	3	
甲組 選修 高等電子材料學(Advanced 3 — 3 Z組 選修 微波積體電路設計(Microwave 3	3 =	e	3	=	3	
Group A Elective Elective Elective (Nonvolatile Memories and Their Fabrication Technologies) 3 Z組 (Embedded System) 選修 (Embedded System)	3 =		3	=		3
甲組 選修 半導體審射(Semiconductor 3 — 3						
甲組 選修 雷射物理(Laser Physics) 3 — 3						
Group A Elective H3719至10000 甲組 選修 場效半導體電子元件(Field- 3 3		T				
Group A Elective Effect Semiconductor Devices) 甲組 選修 半導體光學(Semiconductor 3 — 3						
Group A Elective Optics) 甲組 選修 材料分析(Material Analysis) 3 — 3		T				

						,		 		
пап	選修	微機電元件與系統								
甲組	100000000000000000000000000000000000000	(Microelectromechanical Device	3	_		3				
Group A	Elective	and System)								
甲組	選修	先進積體電路技術(Advanced	_							
Group A	Elective	Integrated Circuit Technology)	3	-		3				
甲組	選修	固態感測元件(Solid-State								
Group A	Elective	Sensors)	3	_		3				
		積體電路技術可靠性工程								
甲組	選修	(Reliability Engineering of	3			3				
Group A	Elective	Integrated Circuit Techology)								
	T	液晶顯示器薄膜製程技術								
甲組	選修	(Liquid-Crystal Displays Thin	3	_		3				
Group A	Elective	Film Process Technology)								
10000000000	200000	先進高介電層材料及應用								
甲組	選修	(Advanced High-K Material and	3	_		3				
Group A	Elective	Application)								
甲組	選修									
Group A	Elective	基礎群論(Basic Group Theory)	3	-		3				
甲組	選修	固態電子學(Solid State								11-2
Group A	Elective	Electronics)	3	_		3				
		微電子構裝技術								
甲組	選修	(Microelectronics Packaging	3			3				
Group A	Elective	Technology)								
甲組	選修	光電半導體元件								
Group A	Elective	(Optoelectronic Semiconductor	3			3				
甲組	選修	半導體製造科技(Semiconductor								
Group A	Elective	Manufacturing Technology)	3			3				
Charles and the second		半導體元件製造與發展實務								
甲組	選修	(Semiconductor Device Fabrication	3			3				
Group A	Elective	and Development Practice)								
甲組	選修	半導體元件物理及特性(Physics and	_							
Group A	Elective	Characterization of Semiconductor Devices)	3	_		3				
甲組	選修	專案實習(Internship to Industry						1		
Group A	上述1多 Elective	Related Project)	4	-		4				
U細 U細	選修	化合物半導體(Compound								
Group A		10百初十等殖(Compound Semiconductor)	3	=	3					
Group A 甲組		先進半導體元件(Advanced								
Group A		Semiconductor Device)	3	=	3					
F 組	選修	積體電路製程實務(Advanced								
Group A	Elective	Topics in VLSI Processing)	3	=	3					
Group A	FIECTIVE	半導體元件製程與實務								
甲組	選修	一等腹儿汁裂往與貝奶 (Semiconductor device process	3	=	3					
Group A	Elective	(Semiconductor device process and practices)	3		ر					
		and practices) 半導體記憶元件導論								
甲組	選修	干导腹記憶元件等調 (Introduction to semiconductor	3	_	3					
Group A	Elective	· · · · · · · · · · · · · · · · · · ·)	-)					
		memory devices)			L. C. Carriero		 		 	

備註 Remark

- 一、畢業學分36學分(含必修6學分、選修24、論文6學分)。1. Graduation credits: 36 credits (including 6 credits of compulsory courses, 24 credits of elective courses, and 6 credits of master's thesis).
 - (1)必修6學分(含學報討論4學分、專題研究2學分) (1) 6 credits of compulsory courses (including 4 credits of "Seminar" and 2 credits of "Project")
- (2)選修24學分(選修他系研究所課程至多承認6學分)。(2) 24 credits of elective courses. A maximum of 6 credits can be admitted for elective graduate courses of other departments.
- (3)論文6學分(通過學位考試並繳交通過審核論文後給予) (3) 6 credits for thesis (given after passing the degree examination and submitting the approved thesis)
- 二、須達英文畢業門艦方可畢業:講詳見長庚大學工學院碩士班研究生英文能力檢測實施方案。2. Students must pass the English proficiency test before you can graduate: please refer to the regulation of the English proficiency test for graduate students of the College of Engineering.
- 三、必修學分6學分:3. Compulsory credits: 6 credits:
- 1.一年級「學報討論」每學期均必修1個學分,共2學分。1. The first-year "Seminar" is required to take 1 credit each semester, a total of 2 credits.
- 2. 二年級每學期均必須參加「學報討論」且及格,但提早畢業及已辦抵免者可不受此限。2. Second-year students must participate in the "Seminar" every semester and pass, but those who graduate early and have already applied for credit are not subject to this restriction.
- 四、選修學分24學分: 甲組為奈米元件及製程、乙組為電子電路設計。 4. Elective credits: 24 credits: Group A is nano-components and manufacturing process, and Group B is electronic circuit design.
- 1. 甲組學生應修通過甲組課程或共同選修課程至少18學分(含),乙組學生應修通過乙組課程或共同選修課程至少18學分(含),若為**外籍生管道人學或**雙 聯學位者,選修課程可不分組別。1. Students in Group A should take at least 18 credits (inclusive) of Group A courses or common elective courses, and at least 18 credits (inclusive) of Group B students should pass Group B courses or common elective courses. Admission for International Students or dual degree program, elective courses may not be divided into groups.
- 2. 「企業實習(1)、(2)」, 最多承認為系定選修學分6學分 2. "Industry Training (1) and (2)", a maximum of 6 credits can be recognized as elective credits.
- 3. 學院共構選修課程列人他系選修。3. The co-constructed elective courses of the College of Engineering can be included in the elective courses of other departments.
- 4.全英文課程之選修課程如附表得認定為畢業學分。The elective courses for full English courses as shown in the attached table can be recognized as graduation credits.
- 五、學生畢業前必須選修基礎課程與核心課程。5. Students must take basic courses and core courses before graduation.
 - 1.甲組基礎課程:半導體元件及物理。1. Group A basic courses: Semiconductor Devices and Physics
 - 2.乙組基礎課程:超大型積體電路設計、被動微波電路,經指導教授同意選定一門。2. Group B basic courses: VLSI Design, Passive Microwave Circuit Design, one selected with the consent of the supervisor.
 - 3.各組其他選修課程經指導教授同意選定兩門為核心課程。3. Two other elective courses of each group are selected as core courses with the consent of the supervisor.

11~學年度第一次教務會議通過

六、修習「記憶體專業學程」課程並完成指定學分者,得認列為本系碩士班畢業學分。學生仍須修滿本系碩士班必修課程及論文(或技術報告替代論文),以符合本系碩士班畢業之要求。 6. Those who have completed the "Memory Program" course and completed the designated credits can be recognized as graduation credits for the master's program. Students still have to complete the required courses and thesis (or technical report substitute thesis) to meet the graduation requirements of the master's program.

the master's program. 七、修習「記憶體專業學程」之「企業實習(1)(2)」,可替代本系碩士班「學報討論」,至多承認2學分。7. Taking "(Industry Training (1) (2)" of the "Memory Program" can replace the "Seminar" of the master's program, and a maximum of 2 credits can be admitted.

主管簽名:

2024.04.29